

REMARKS

The Attorney for Applicants wishes to thank the Examiner for the telephone conversation on 1/16/03 in which a discussion was had on the definition of a signal and a signal selectively present and wherein the Examiner suggested that the phrase "signal carrier" may more aptly describe the present invention.

Applicant respectfully requests reconsideration of this application, in view of the following remarks.

The proposed drawing correction to Fig. 3C, filed on 9/26/02, stands disapproved and Figure 3c stands objected to as duplicative of Figure 3b.

Claims 1-11 stand rejected under 35 USC § 102(e) as being anticipated by U.S. Patent 6,115,823 to Velasco et al. (Velasco).

In the Drawings

Applicants have requested removal of Figure 3c. Applicants request approval of the removal of Figure 3c and withdrawal of the objection. If the Examiner approves of the removal of Figure 3c, notification of such approval to the draftsman, per enclosed letter is requested.

In the Specification

The Specification has been amended to remove any reference to Figure 3c. No new matter has been added. Applicants request approval of the specification as amended.

In the Claims

Claims 1-11 stand rejected under 35 USC § 102(e) as being anticipated by U.S. Patent 6,115,823 to Velasco et al. (Velasco).

Claims 1-11 have been amended to more particularly point out and distinctly claim the subject matter that Applicants regard as the invention by redefining a prior limitation in the originally filed claims with additional language to delineate that prior limitation. Support for “at compilation” may be found in the description on page 10, in Figure 6 at step 625, and throughout the specification.

Applicants hereby reserve the right to swear behind the cited references.

Claims 1-11 rejection under 35 U.S.C. § 102(e)

Applicants submit that the Action is equating two different language terms and thereby improperly arriving at an equivalence of elements in the Applicants' claims and the cited art. As noted, Applicants have amended the claims to more clearly delineate this distinction.

Specifically, the Action on page 4 states:

Regarding claim 1, Applicants argue that "Velasco does not disclose an interface signal or a group of signals that are selectively present at the interface." However, Velasco clearly teaches at column 35, lines 50-60 that some signals are selectively present. For example, for parametrized RAM size, when a smaller size RAM is used, the signals for the larger size RAM are not used.

(Emphases added.)

The Action has equated the term “selectively present” with “used or not used”. These are different terms and do not equate to one another.

To illustrate this distinction by way of an example, using the parameterized RAM size of Velasco above; when a smaller size RAM is used, the signals for the larger size RAM are not used, however the signals for the larger size RAM are still present. In one embodiment of the Applicants' invention, if a smaller size RAM is used, only the signals for the smaller size RAM are present, the signals for the larger size RAM are not present.

Claim 1 rejection under 35 U.S.C. § 102(e)

Applicants amended claim 1 recites:

1. (Amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be selectively present.

(Emphasis added.)

Applicants submit that Velasco does not disclose, “at least one interface signal that is configurable, at compilation, such that the interface signal can be selectively present” as in Applicants’ claim 1. For this reason Applicants submit that claim 1 is not anticipated by the cited art and that claim 1 and claims dependent on claim 1 are allowable over the cited art.

Claims 4, 6, 7, and 9 rejection under 35 U.S.C. § 102(e)

Claims 4, 6, 7, and 9 claim among other things, the “at compilation” limitation. As discussed above with respect to claim 1, Applicants submit that these claims are not anticipated by the cited art. Applicants submit that claims 4, 6, 7, and 9 and claims dependent on these claims are allowable over the cited art.

CONCLUSION

Applicant respectfully submits that all claims are in condition for allowance, and requests allowance of all claims.

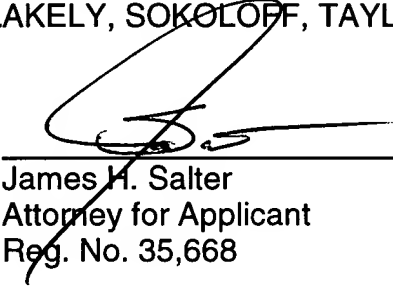
The Examiner is invited to call James H. Salter at 408 720-8300 x329 if there remains any issue with allowance.

Please charge any shortage to our Deposit Account No. 02-2666 and take any extension of time necessary.

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Claims

Please amend the claims as follows:

1. (Amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be selectively present.
2. (Amended) The computer core as set forth in claim 1, wherein at least one interface signal capable of being carried by said at least one interface signal carrier is further configured to support different levels of functionality.
3. (Amended) The computer core as set forth in claim 2, wherein a signal carrier width of the at least one interface signal carrier is configurable to support different signal widths.
4. (Amended) A computer core comprising at least one interface signal carrier that is configurable, at compilation, such that the at least one interface signal carrier can be configured to support different levels of functionality.
5. (Amended) The computer core as set forth in claim 4, wherein a signal carrier width of the at least one interface signal carrier is configurable to support different signal widths.
6. (Amended) A computer core comprising at least one interface signal carrier, a signal width of the at least one interface signal carrier being configurable at compilation to support different signal widths.
7. (Amended) A method for generating at compilation a computer core interface, the method comprising:

providing configurable source code representative of the computer core interface and identifying parameters of the computer core interface;

defining configuration parameters of the computer core interface; and

generating a computer core from the configurable source code representative of the computer core interface comprising an interface comprising the identified parameters of the computer core interface configurable in accordance with the defined configuration parameters of the computer core interface.

8. (Amended) The method as set forth in claim 7, wherein at least one of the configuration parameters of the computer core interface ~~is~~ are selected from the group ~~comprising~~ consisting of parameters that define whether ~~the~~ a signal carrier is present in the computer core interface, define different levels of functionality of the computer core interface, and define a signal width of at ~~least~~ least one computer core interface signal.

9. (Amended) A method for generating at compilation a core with a configured interface, the method comprising:

implementing the core as configurable source code utilizing at least one defined parameter of an interface of the core;

selecting at least one configuration option of the at least one defined parameter of the interface of the core;

generating ~~a~~ the core comprising ~~a~~ the at least one defined parameter of the ~~core~~ interface of the core that operates in accordance with the selected interface comprising the selected at least one configuration option.

10. (Amended) The method as set forth in claim 9, wherein selecting at least one configuration option of the at least one defined parameter of the interface of the core is performed through a graphical user interface.

11. (Amended) The method as set forth in claim 9, wherein selecting at least one configuration option of the at least one defined parameter of the interface of the core is performed by deriving the at least one configuration option of the at least one

~~defined parameter of the interface of the core configuration options from the a
configurations configuration option of another core.~~